**TEMIC** Semiconductors

#### MATRA MHS

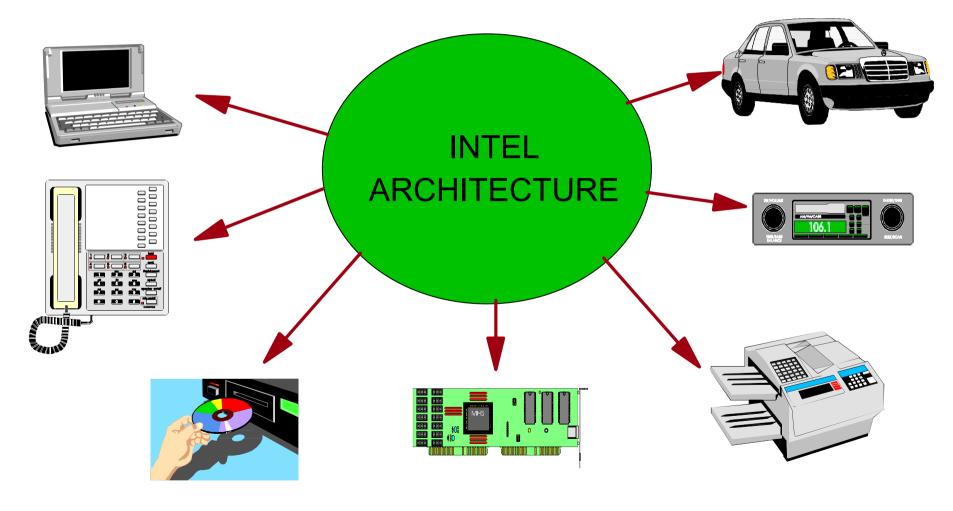
#### C 251 8 bit microcontroller family

Etienne BENETEAU Product Marketing

November 1994

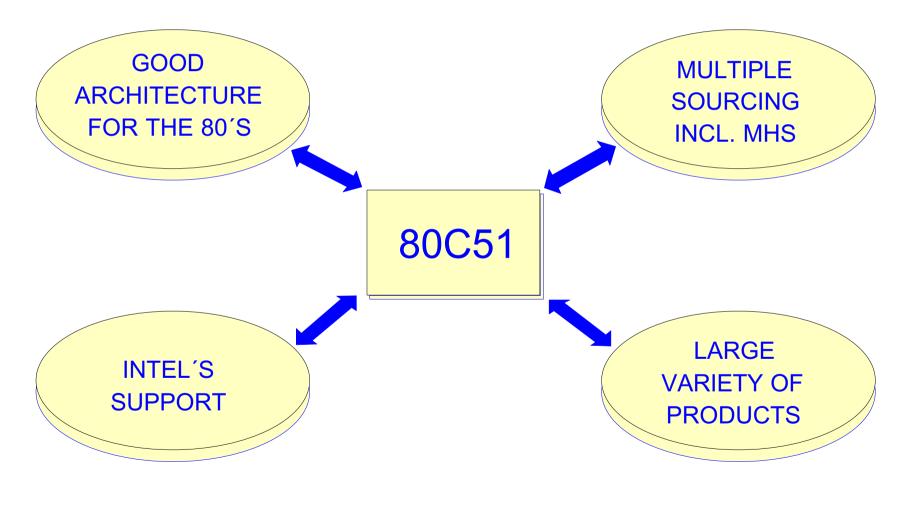


#### THE MICRONCONTROLLER IS USED EVERYWHERE





#### WHY WAS THE C51 FAMILY SO SUCCESSFUL



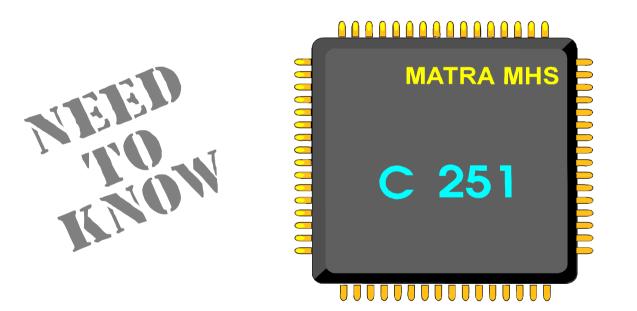


#### NEW REQUIREMENTS FOR THE 90'S AND BEYOND

- HIGHER PERFORMANCES TO SUPPORT SOPHISTICATION OF EQUIPMENT
- EXTENDED ADDRESSSING SPACE
- EASIER PROGRAMMING WITH "C" LANGUAGE
- TIME-TO-MARKET WITH OWN SPECIFICATION
- LESS POWER CONSUMPTION AND NOISE
- RE-USE OF EXISTING CODE DUE TO BIG DEVELOPMENT INVESTMENTS AND SOFTWARE QUALIFICATION
- SYSTEM COST REDUCTION



# C 251, THE SUPER 8-BIT FAMILY



# THE NEW MICROCRONTROLLER GENERATION



#### PARTNERSHIP WITH INTEL

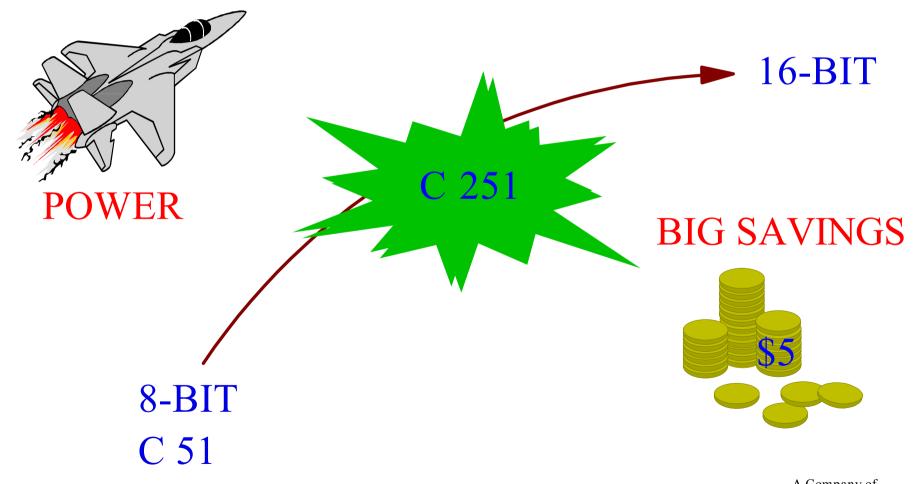
## INTEL



# TEMIC MATRA MHS

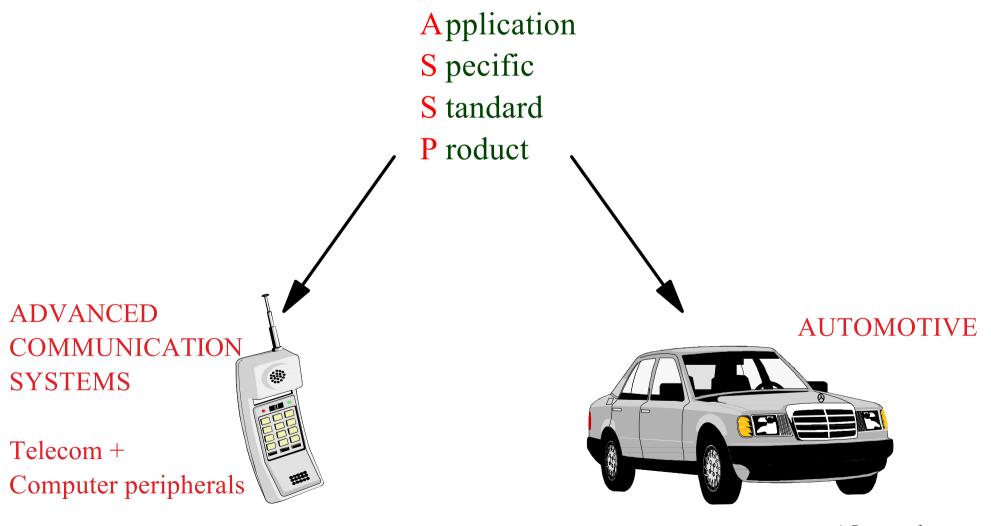


#### PRODUCT POSITIONING





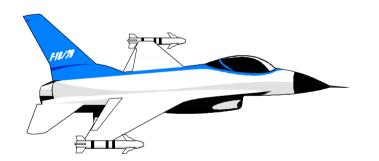
TARGETS





#### PERFORMANCE HIGHLIGHTS

### 5 TO 15 TIMES FASTER THAN THE 80C51 MICROCONTROLLER



### HIGH INSTRUCTION THROUGHPUT AT LOW CLOCK SPEED

#### **BENEFITS : POWER CONSUMPTION AND RFI REDUCED**



#### CPU IMPROVEMENTS

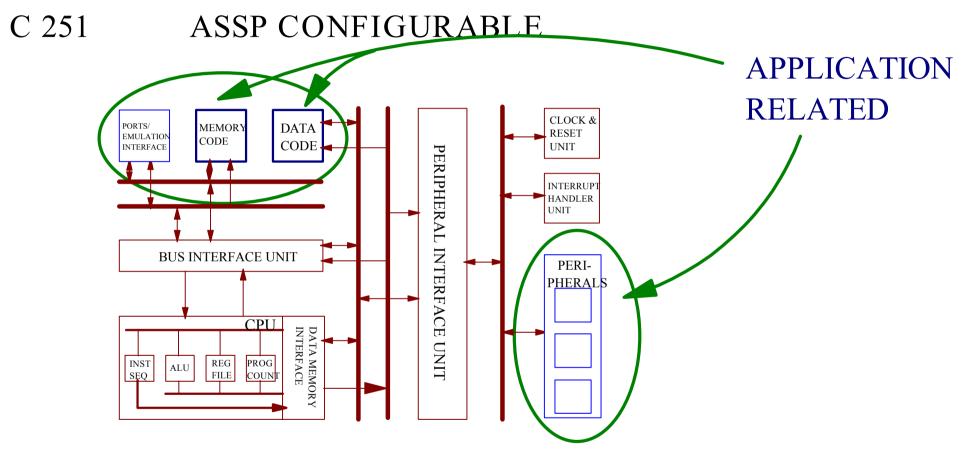
### VERY FAST: 2 CLOCK CYCLES PER STATE



#### **3-STAGE PIPELINE ARCHITECTURE**

**16-BIT INTERNAL CODE BUS** 





# ASSP STRATEGY BASED ON LARGE FLEXIBILITY DEFINE YOUR SYSTEM



#### THIRD PARTY DEVELOPMENT TOOLS

## WORLDWIDE REPRESENTATION (Keil, BSO tasking, Metalink, Nohau...)



X

COMPLETE PACKAGE OF TOOLSSIMULATORAvailableEMULATORSAvailableASSEMBLERAvailableC - COMPILERAvailableROM MONITORQ4/95



#### C 251 ROADMAP **VOLUMES** 80C251XX 80C251XX 80C251C2 800 80C251XX 80C251C4 80C251XX 80C251A2 80C251C1 80C251C3 80C251G0 80C251A1 Simulator Emulator DSP LIB 1996 1997 1998 1995



#### C 251 SOLE SOLUTION

- With best cost/performance ratio
- To address applications requested on the market
- To support "C" language programming with efficiency
- With code compatibility to preserve investments

